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# Application Driven Evaluation of Fault-Tolerant Quantum Computing Architectures

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IPAM Workshop  
2/19/2026

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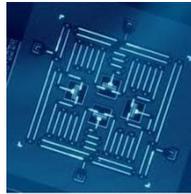
# Analyzing NISQ and Fault-Tolerant Architectures

## NISQ Technologies

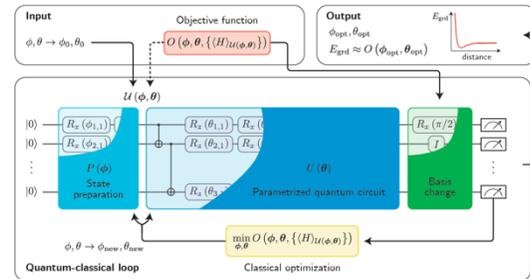
### Google Sycamore



Transmon qubit



### VQE Algorithm(1)

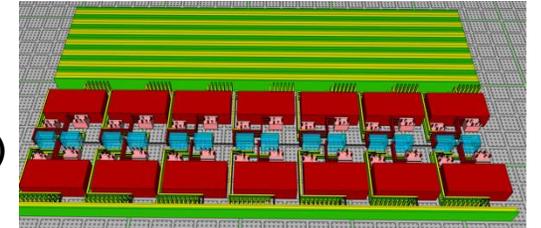


### Quantum Race-Track Architecture

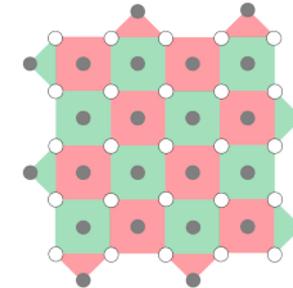


## Fault-tolerant QC Technologies

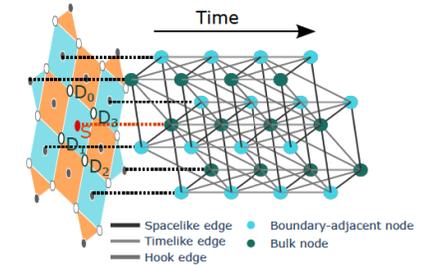
### Factoring Machine Architecture (2)



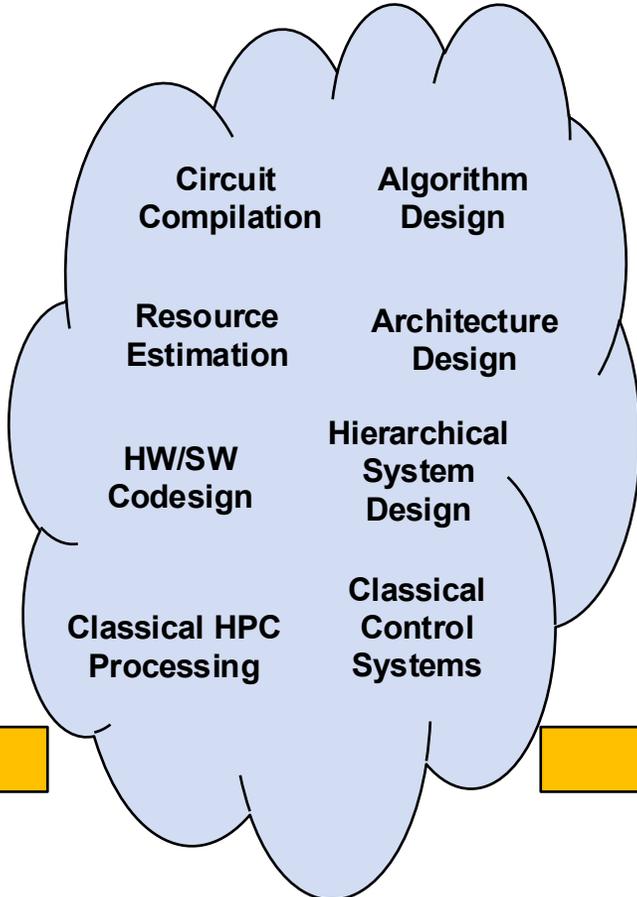
### Surface Codes



### Syndrome Decoders (3)



### IBM Bluejay Concept



**Shared need for Design, Analysis, and Classical tools across NISQ and FT platforms (even though the details may be different)**



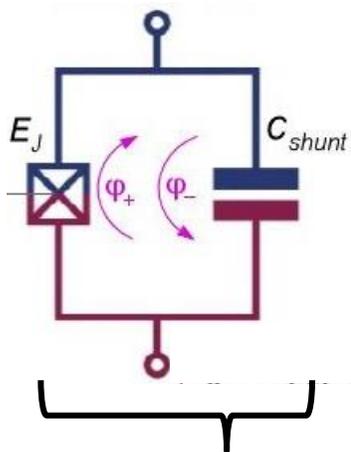
# Outline

- **Architecture Analysis of near-term architectures**
- **Generating logical circuits for utility-scaled applications**
  - Mapping to quantum processors
  - Generating circuits with pyLIQTR
- **Analyzing Fault-tolerant architectures**
  - Gate counting to determine algorithmic scaling
  - Circuit scheduling to emulate the impact of hardware constraints
- **Open questions in quantum architectures**

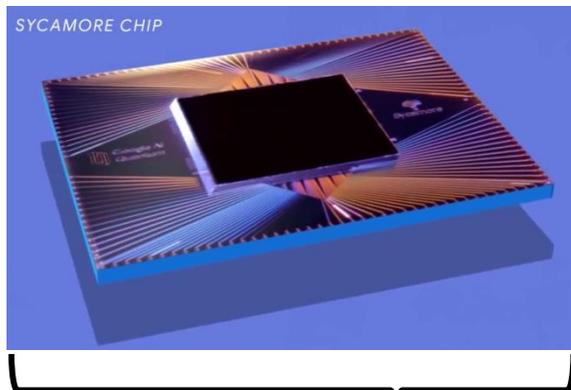


# Near-term Quantum Computing Architectures

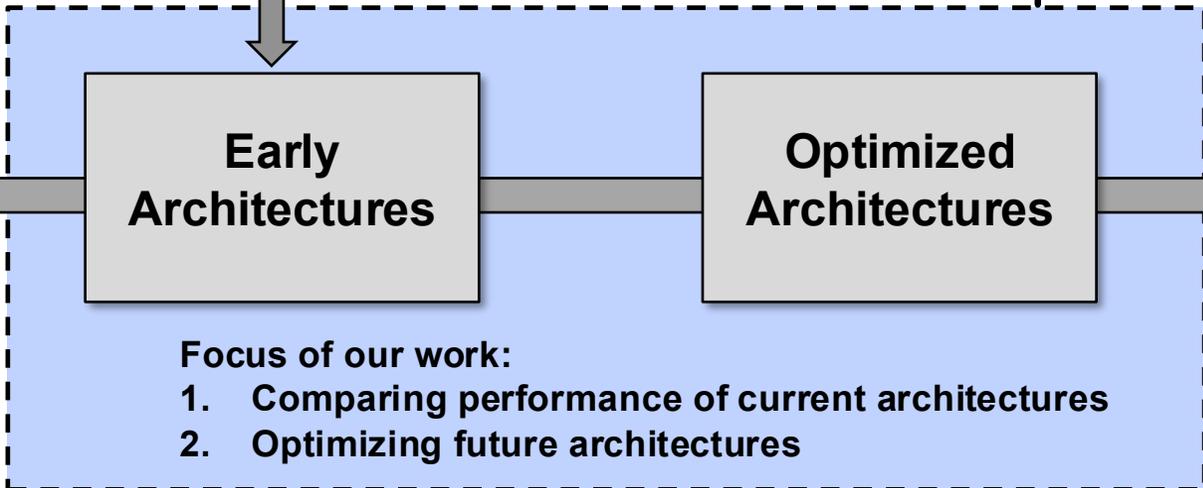
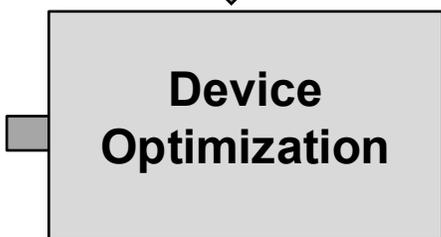
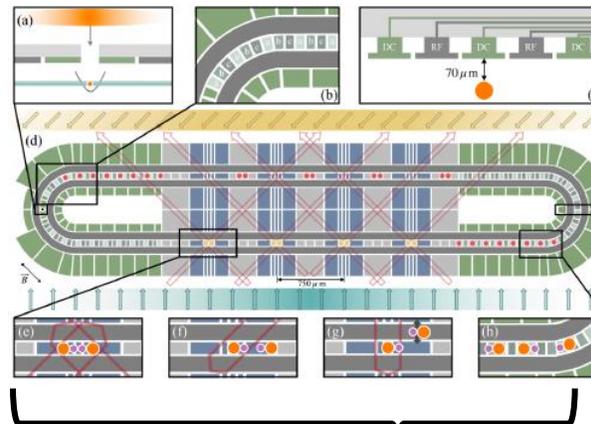
## Transmon Qubit



## Sycamore Processor



## Quantinuum H2 Architecture



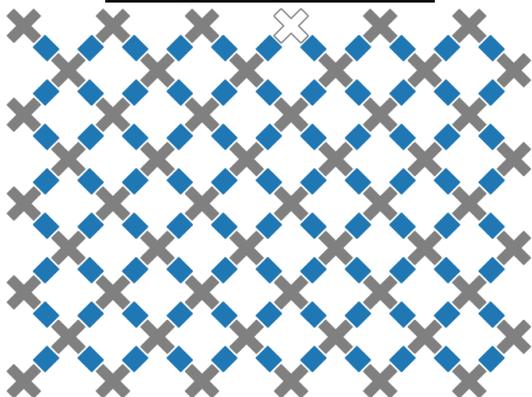
**Benchmarking current and emerging architectures will enable optimization of future designs**



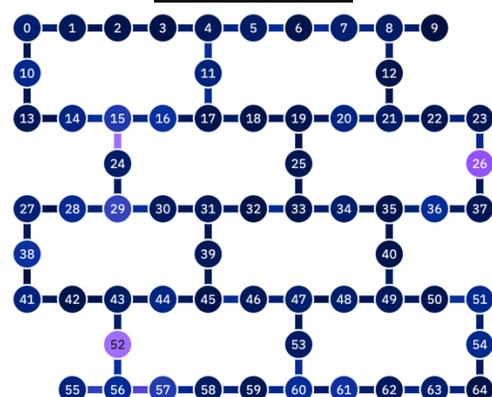


# Analysis of QAOA on Superconducting Qubit Architectures (1)

Google Sycamore<sup>1</sup>

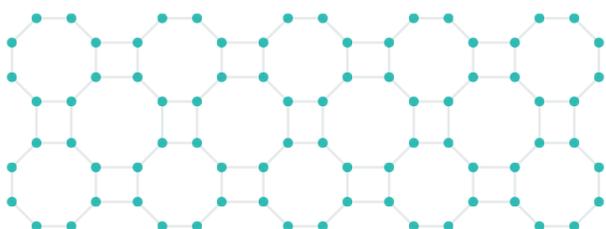


IBM Ithaca<sup>2</sup>

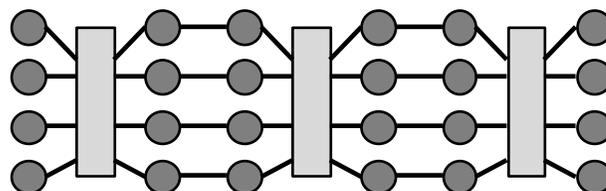


Commercially available architectures and envisioned future ones

Rigetti Aspen M3<sup>3</sup>

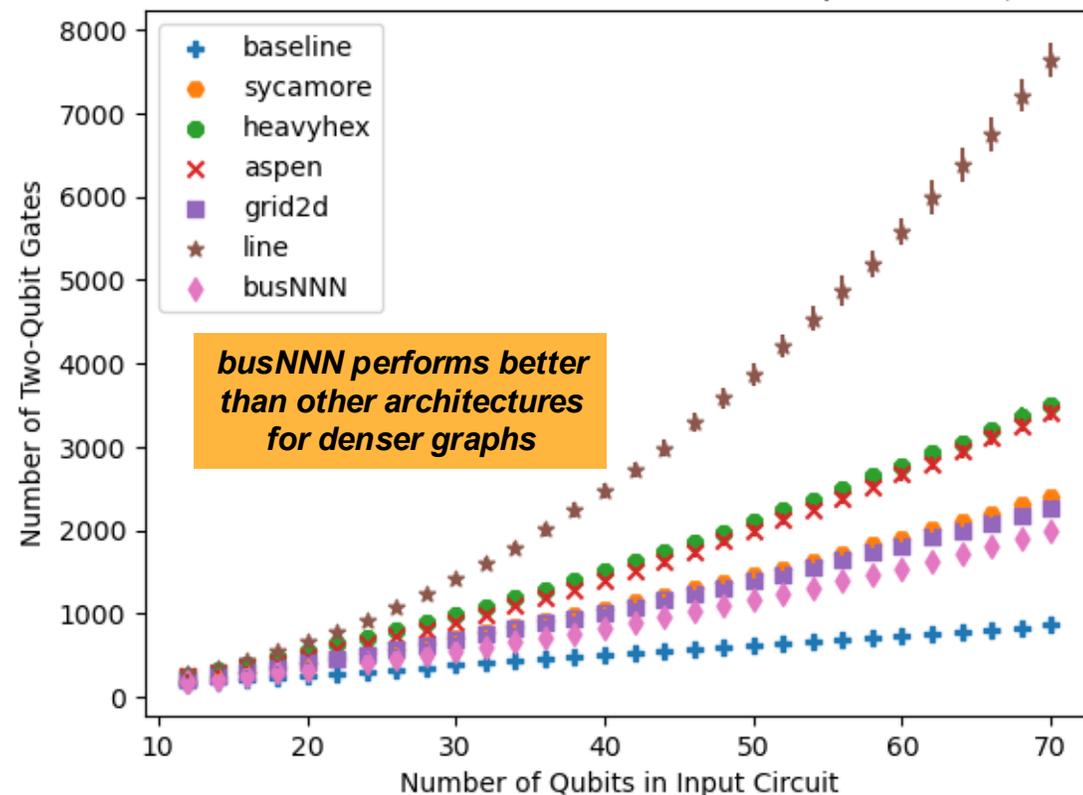


busNNN Architecture



Bus size = 8, Total busses = 3

Number of Two Qubit Gates vs. Number of Input Qubits (12REG)



1) B. Rempfer, et.al. "Comparison of superconducting NISQ architectures." IEEE QCE 2023

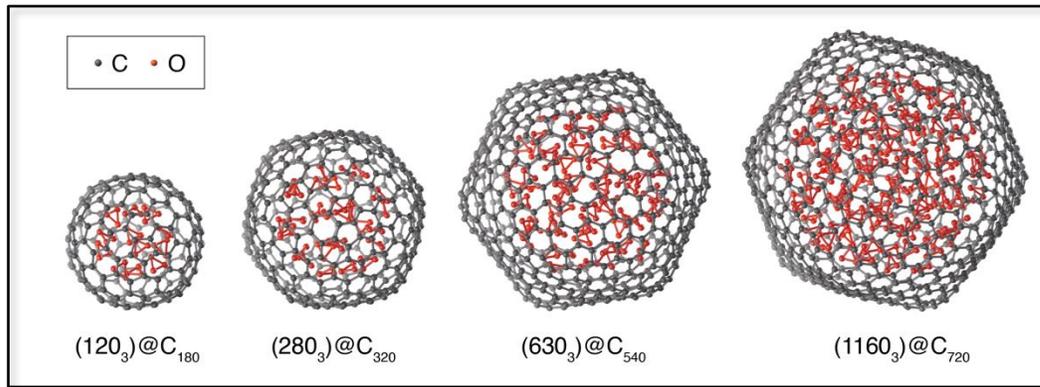


# Outline

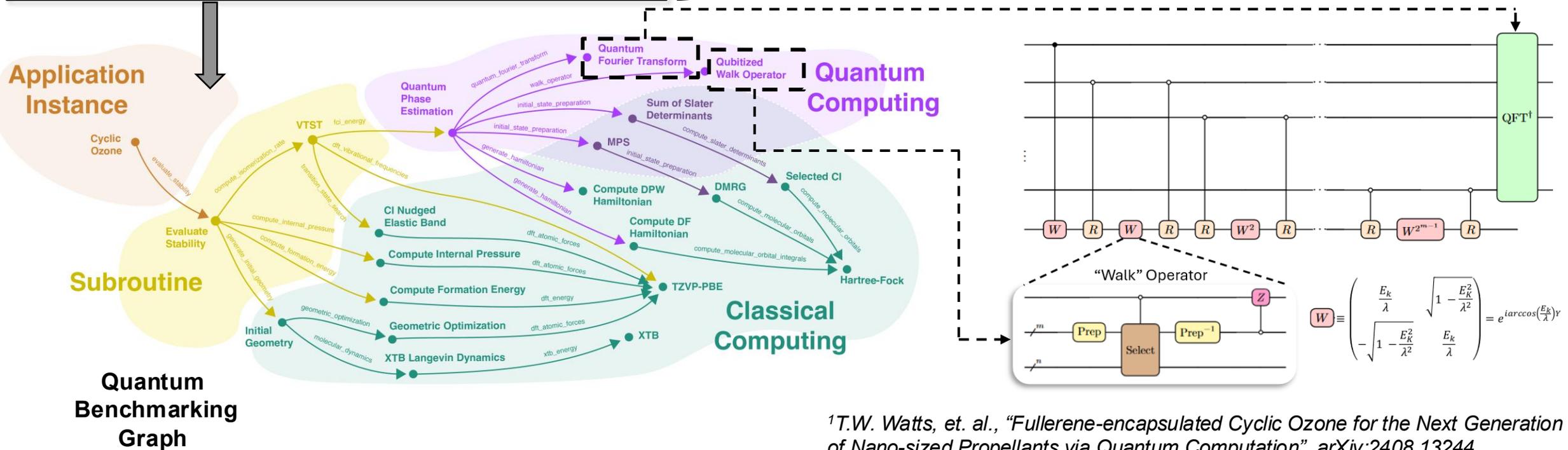
- Architecture Analysis of near-term architectures
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  - Mapping to quantum processors
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- Open questions in quantum architectures



# Partitioning Application Instances (Cyclic Ozone<sup>1</sup>)



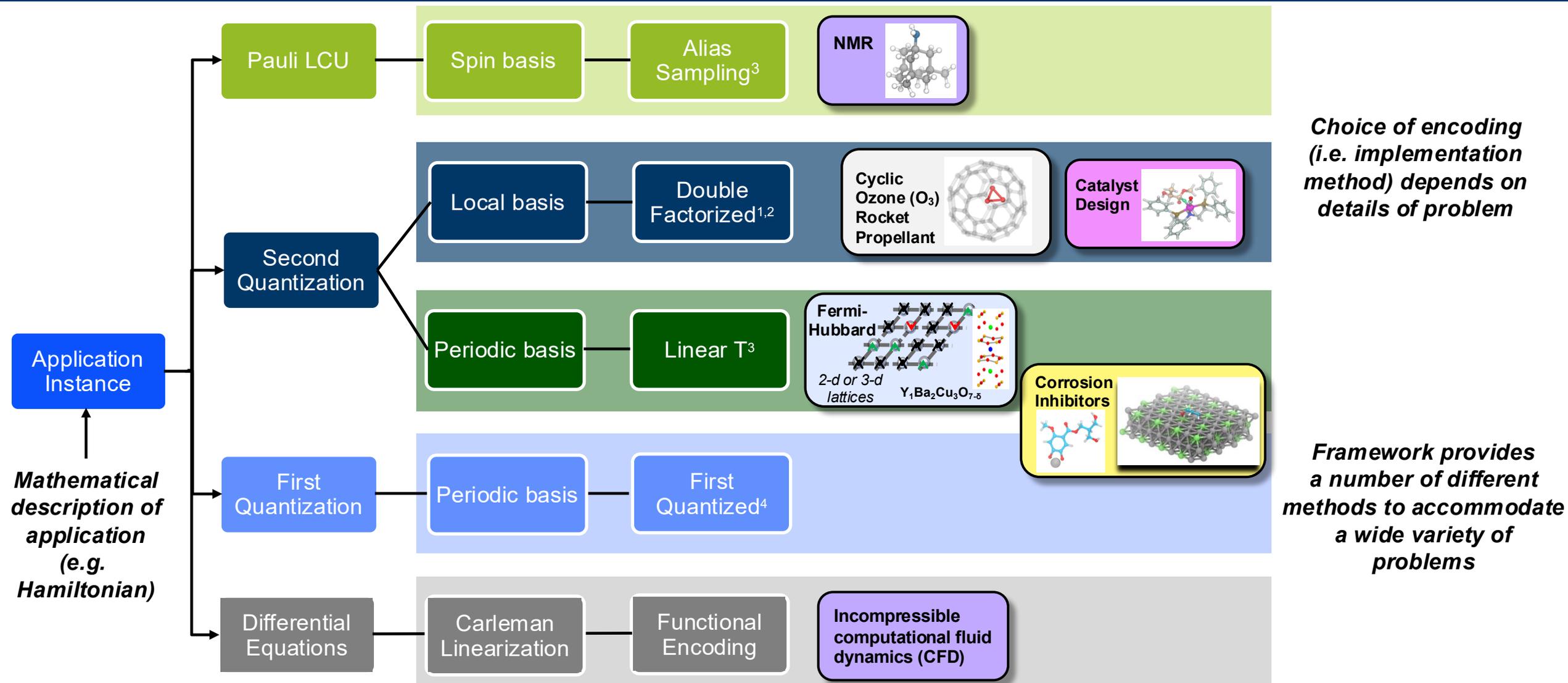
Geometries for ozone-containing endofullerenes. Geometries were obtained, and optimized, using Density Functional Theory (DFT)



<sup>1</sup>T.W. Watts, et. al., "Fullerene-encapsulated Cyclic Ozone for the Next Generation of Nano-sized Propellants via Quantum Computation". arXiv:2408.13244



# Encoding and Implementing Applications





# pyLIQTR (Opensource SW for Generating Circuit Implementations)

## Library Structure

pyLIQTR

ProblemInstance

Chemical Ham.  
Electronic Struct.  
Lattice

*Hamiltonian  
creation*

Block Encodings

DoubleFactorized  
LinearT  
Carleman

*Block Encoding  
and Circuit  
creation*

Algorithms

QSP/QSVT  
GSEE, Dynamics

*Algorithm  
Circuit  
Implementation*

Classical prep

Angle Gen.  
PEST  
Clifford+T syn.

*Phase angle  
generation, rotation  
gate synthesis, etc.*

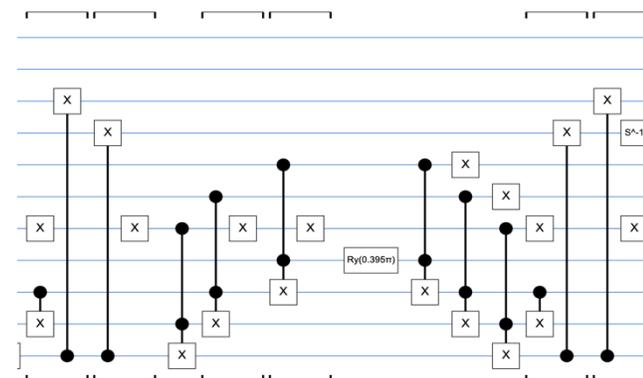
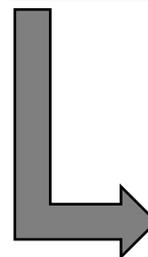
## pyLIQTR Version 1.4.2



`pip install pylqtr`

pyLIQTR (Lincoln laboratory Quantum algorithm Test and Research) is a python-based tool-kit that contains implementation of quantum algorithms, classical pre-processing, circuit generation, and resource analysis.

```
from pyLIQTR.ProblemInstances.getInstance import *
from pyLIQTR.BlockEncodings.getEncoding import *
from pyLIQTR.utils.resource_analysis import estimate_resources
from pyLIQTR.utils.circuit_decomposition import circuit_decompose_multi
from pyLIQTR.qubitization.phase_estimation import QubitizedPhaseEstimation
from pyLIQTR.qubitization.qubitized_gates import QubitizedWalkOperator
from pyLIQTR.utils.printing import openqasm
```



*Logical Circuit  
Implementation  
in Google Cirq  
and Qualtran*

quantumlib/  
Qualtran





# Application Problems (pyLIQTR Notebooks)

- **Homogeneous catalysis**
  - Double-factorized Block encoding
- **Fermi Hubbard**
  - Linear-T block encoding
- **Local Chemistry**
  - Pauli-LCU block encoding
- **Periodic Chemistry**
  - Linear-T electronic structure encoding
- **Non-Linear ODE**
  - Carleman linearization
- **Heisenburg**
  - Pauli-LCU block encoding

The collage includes:

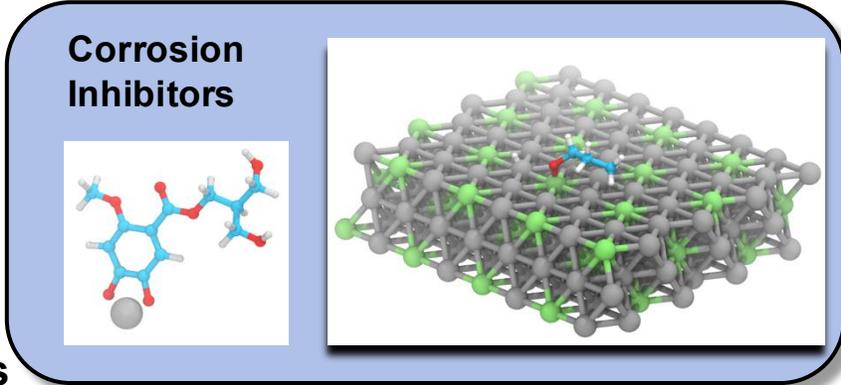
- A quantum circuit diagram for a Trotterized evolution with CNOT gates and phase shift operations.
- Two graphs showing resource requirements: 'Total T-Count' vs 'Problem Size (Orbitals)' and 'Total Logical Qubits' vs 'Problem Size (Orbitals)'. Both show exponential growth.
- A diagram illustrating the construction of a 3D periodic image from a 2D periodic image and a vacuum gap, used for electronic structure encoding.
- Text from a notebook describing the resource estimation process for a 4x4x2 Mg slab, including the use of the `BlockEncoding` class and the `ProblemInstance` class.
- Python code snippets for setting up the problem instance and generating the circuit.

**Jupyter Notebooks  
for Application  
Generation and  
Resource Analysis**

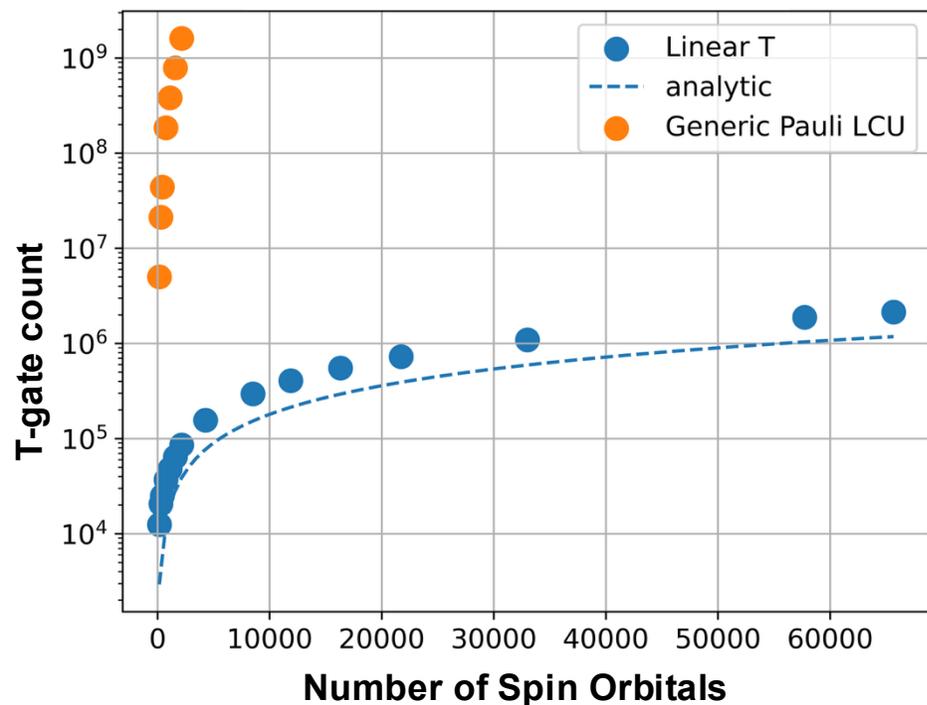


# Block Encoding Comparison: Magnesium Slab (Resources)

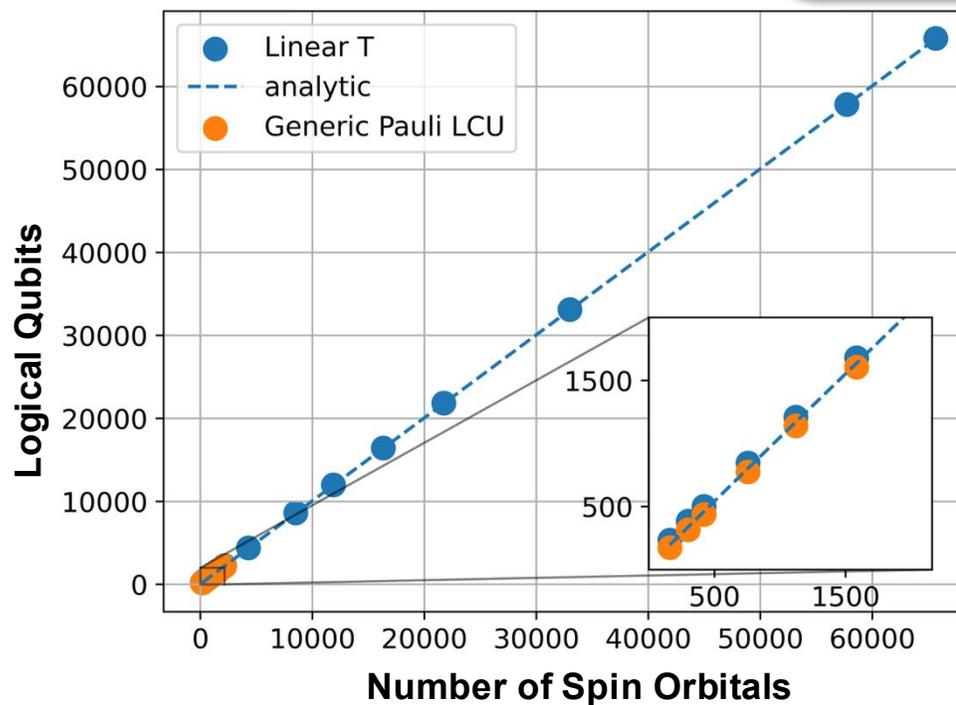
- Linear T encoding reduces T gate count by many orders of magnitude
- Increase in qubit count is modest



### T-count



### Number of Qubits



**Linear T encoding takes two orders of magnitude less time to execute than Pauli LCU**

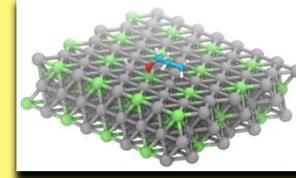
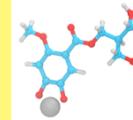
Linear T encoding assumes a phase estimation energy error  $\Delta E = 10^{-3}$  and approximation bits  $\lceil \log(\frac{2\sqrt{2}\lambda}{\Delta E}) \rceil$

**QPE circuit rotation synthesis error budget of  $10^{-4}$**



# Circuit Results for Qubitized Phase Estimation

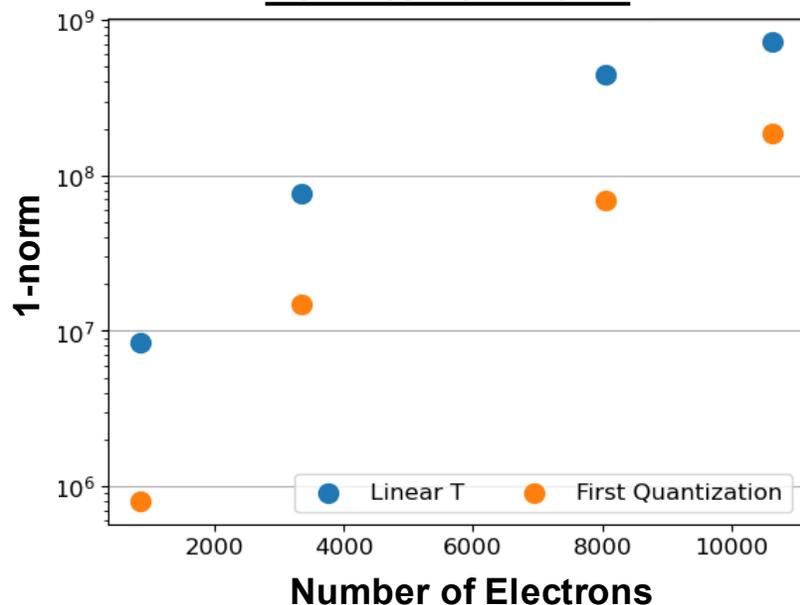
Corrosion Inhibitors



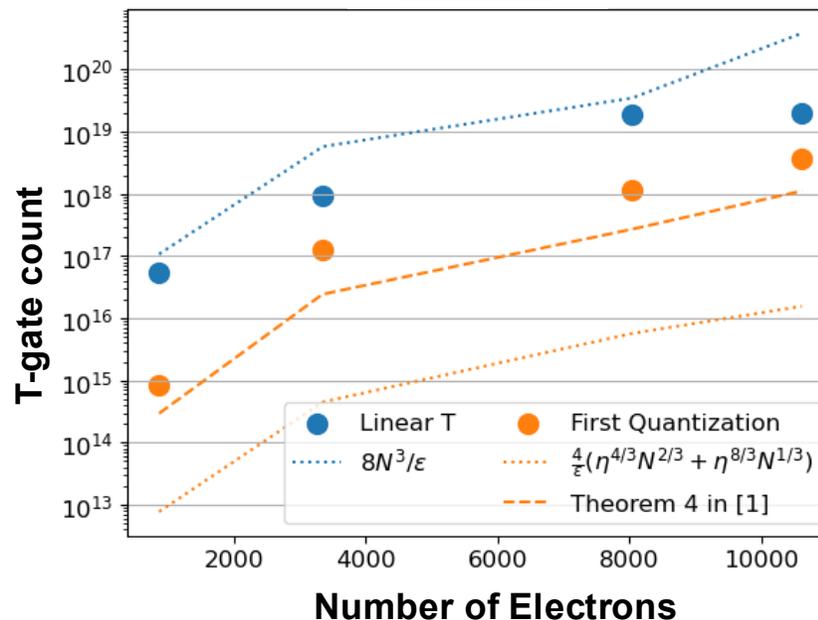
Encodings assume a phase estimation energy error  $\Delta E = 10^{-3}$

Results for target energy cutoff of 40 Ry

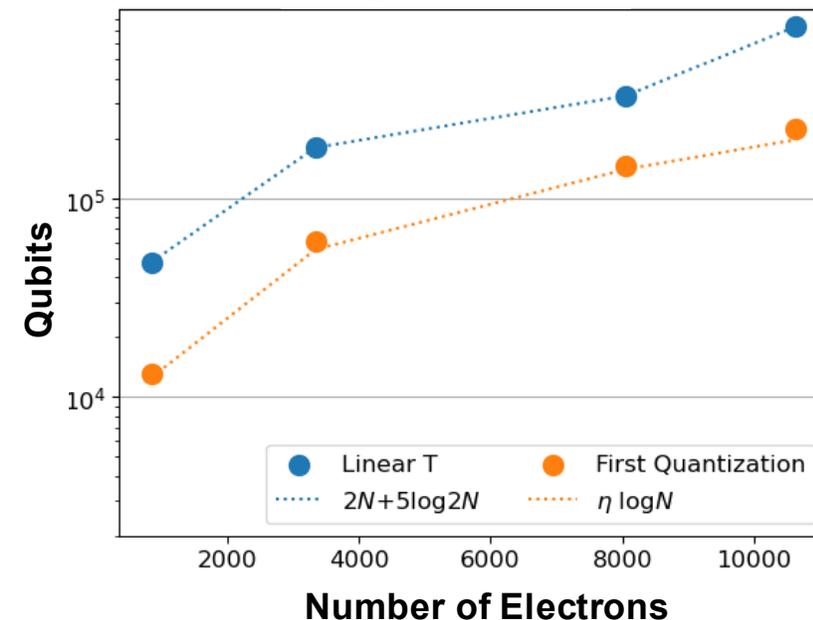
### Hamiltonian Norm



### T-count



### Logical Qubits



- Asymptotic expressions for qubit count match well with circuit estimates
- First quantized qubit count  $\sim 3x$  lower and T-gate count  $\sim 10-100x$  lower than linear T
- Details of circuit composition important for gate counts

First quantized encoding reduces qubit count by 10x and T count by up to 100x



# Outline

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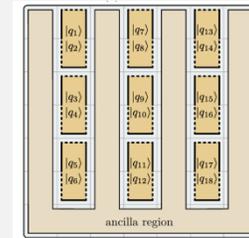
# Architecture Models for Resource Estimation

## Gate Counting

- Circuit decomposed into Clifford+T gates
  - Typically only T gates are reported
- Counts based on analytical formulas or explicit circuits
- Gives a rough estimate of scaling
- Repeated blocks only need counting once

pyLIQTR estimate\_resources

## Game of Surface Codes<sup>1</sup>



- Gates executed via multi-qubit measurements
- Clifford gates pushed to end of circuit and folded into measurements
- Serial execution of T gates

MS Azure Resource Estimation<sup>2</sup>

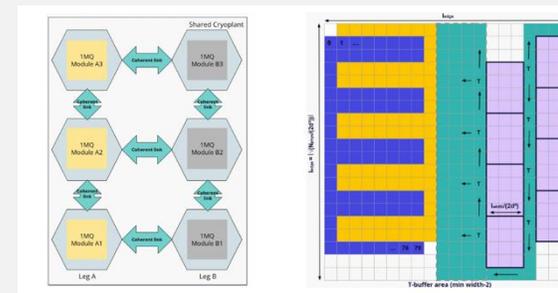
## Distributed Lattice Surgery



- Communication via lattice surgery
- Computation is distributed
- Clifford gates are executed inline

pyLIQTR scheduler

## Graph-State Compilation

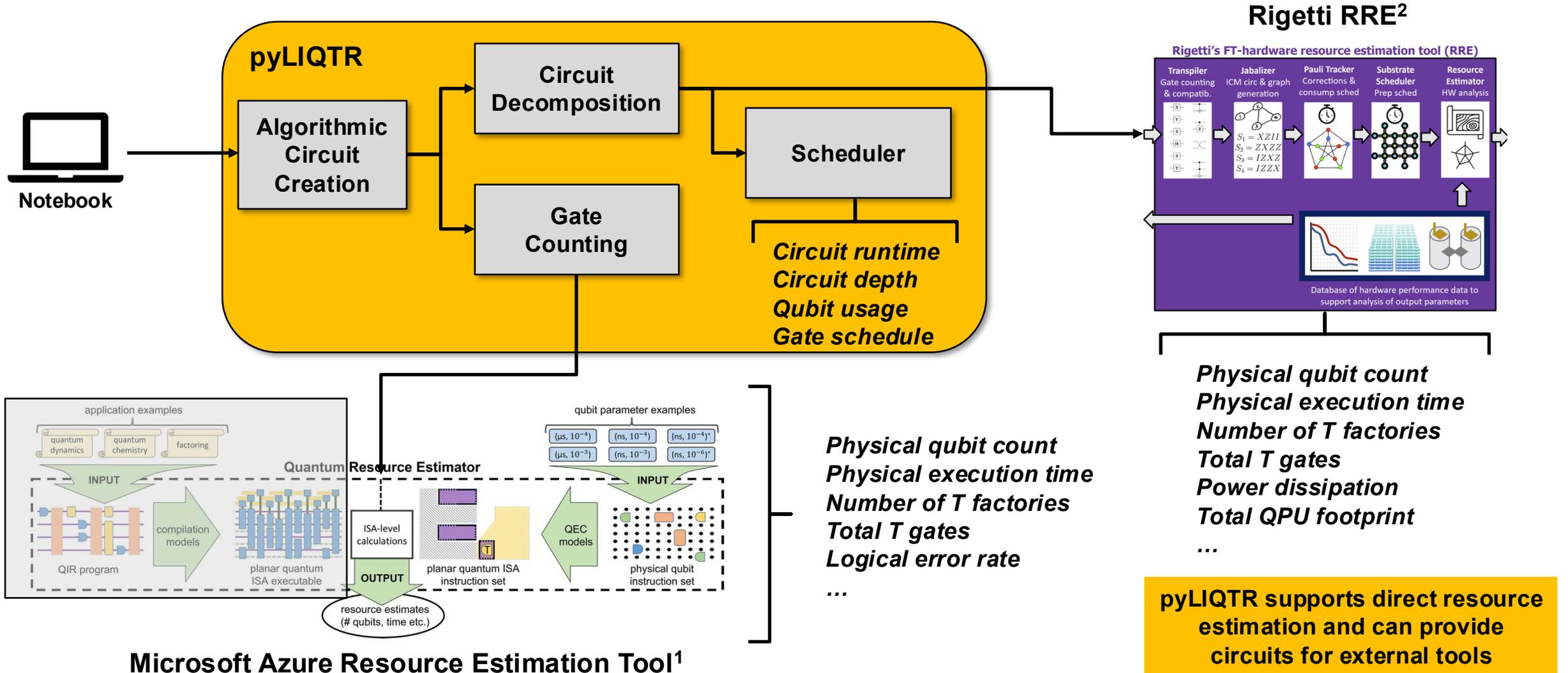


- Circuit compiled into:
  - 1) Graph-state creation
  - 2) Graph consumption
- Compiles onto a measurement-based architecture

RRE<sup>4</sup> (Rigetti)



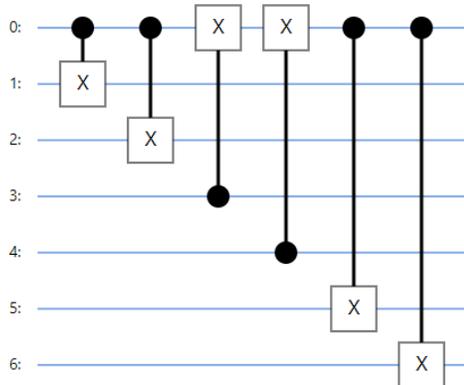
# Using pyLIQTR for Logical & Physical Resource Estimation





# Time-based Scheduling in pyLIQTR

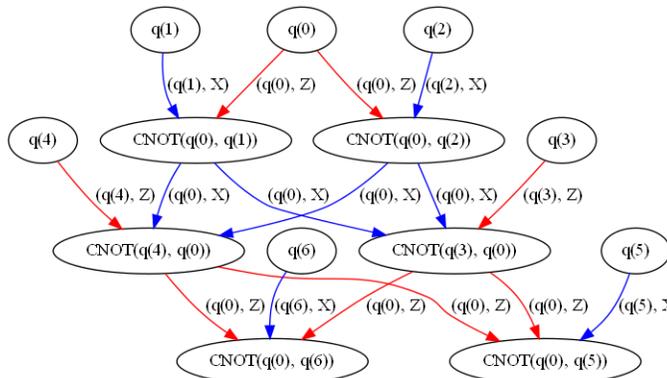
## Input Logical Circuit



Circuit gets compiled to Directed Acyclic Graph (DAG)



## Dependency DAG



Traverse DAG, scheduling ready instructions in time order



## Output Analysis

Execution time: 12  
Circuit T-depth: 0  
Qubits: 7  
Gate profile: {"T": 0, "CX": 6, "Pauli": 0}

### Customizable parameters:

**Resource and architectural specifications:** specifies timing for gate types and structural restrictions on execution of T-gates

**Decomposition level:** Controls the degree of decomposition of circuit

**Decomposition protocol:** Controls how repeated components are accumulated

**Gateset:** specifies which gates to include in analysis

*A time-based scheduler provides a cycle-by-cycle schedule of gate execution*

*This can be used for estimation or to drive a cycle-accurate logical simulation*

### Scheduler results:

**Circuit execution time:** based on operation timings and ordering, total circuit time

**Circuit T-depth:** total number of circuit 'moments' which contain at least one T-gate

**Number of qubits used:** total number of logical qubits

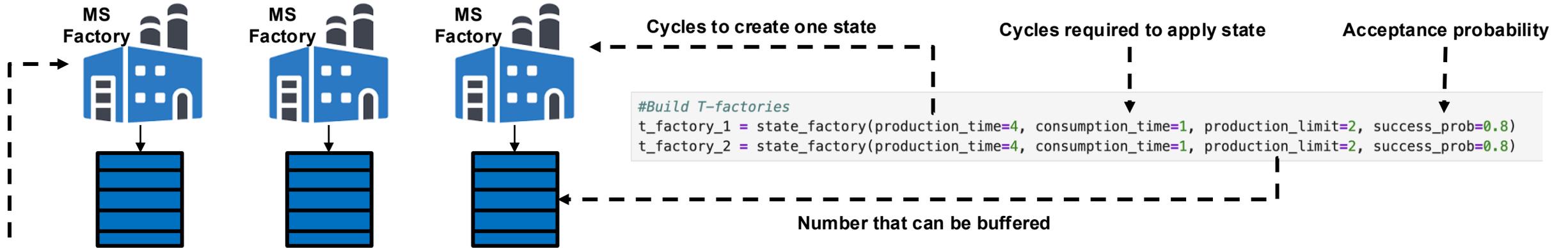
**Gate profile:** a full tally of basic gates in the circuit.

**Active qubit distribution:** Distribution of active qubits

**T-widths:** a distribution of the number of T-gates contained within each time step



# Resource Estimation with the pyLIQTR Scheduler



**Magic State Factory Parameters(1)**

Protocol	$p_{phys}$	$p_{out}$	Qubits	Cycles	Space-time cost per output state	
					Qubitcycles	Full distance
$(15-to-1)_{7,3,3}$	$10^{-4}$	$4.4 \times 10^{-8}$	810	18.1	14,600	$5.49d^3/d = 11$ $3.33d^3/d = 13$
$(15-to-1)_{9,3,3}$	$10^{-4}$	$9.3 \times 10^{-10}$	1,150	18.1	20,700	$4.71d^3/d = 13$ $3.07d^3/d = 15$
$(15-to-1)_{11,5,5}$	$10^{-4}$	$1.9 \times 10^{-11}$	2,070	30.0	62,000	$9.19d^3/d = 15$ $6.31d^3/d = 17$
$(15-to-1)_{9,3,3}^4 \times (20-to-4)_{15,7,9}$	$10^{-4}$	$2.4 \times 10^{-15}$	16,400	90.3	371,000	$27.0d^3/d = 19$ $20.0d^3/d = 21$
$(15-to-1)_{9,3,3}^4 \times (15-to-1)_{25,9,9}$	$10^{-4}$	$6.3 \times 10^{-25}$	18,600	67.8	1,260,000	$25.9d^3/d = 29$ $21.2d^3/d = 31$
$(15-to-1)_{17,7,7}$	$10^{-3}$	$4.5 \times 10^{-8}$	4,620	42.6	197,000	$6.30d^3/d = 25$ $4.04d^3/d = 29$
$(15-to-1)_{13,5,5}^6 \times (20-to-4)_{23,11,13}$	$10^{-3}$	$1.4 \times 10^{-10}$	43,300	130	1,410,000	$28.9d^3/d = 29$ $19.6d^3/d = 33$
$(15-to-1)_{13,5,5}^4 \times (20-to-4)_{27,13,15}$	$10^{-3}$	$2.6 \times 10^{-11}$	46,800	157	1,840,000	$30.9d^3/d = 31$ $21.5d^3/d = 35$
$(15-to-1)_{11,5,5}^6 \times (15-to-1)_{25,11,11}$	$10^{-3}$	$2.7 \times 10^{-12}$	30,700	82.5	2,540,000	$35.3d^3/d = 33$ $25.0d^3/d = 37$
$(15-to-1)_{13,5,5}^6 \times (15-to-1)_{29,11,13}$	$10^{-3}$	$3.3 \times 10^{-14}$	39,100	97.5	3,810,000	$37.6d^3/d = 37$ $27.7d^3/d = 41$
$(15-to-1)_{17,7,7}^6 \times (15-to-1)_{41,17,17}$	$10^{-3}$	$4.5 \times 10^{-20}$	73,400	128	9,370,000	$39.8d^3/d = 49$ $31.5d^3/d = 53$

**Factory parameters specified as a table. Table from (1) used as example, but specification is arbitrary**

1) D.Litinski. "Magic State Distillation: Not as Costly as you Think." Quantum 3, 205 (2019)

**qubit\_block**

↑  
Defines code distance/timing, and #physical qubits

**Block parameters**

```
#to assign specific qubits to blocks:
qb_0 = qubit_block(p=4, m=4, f=4, t=1, qubit_list=['target0', 'target1', 'target2', 'target3'], factories=[t_factory_1])
qb_1 = qubit_block(p=3, m=2, f=8, t=1, qubit_list=['selection0', 'selection1', 'selection2', 'selection3', 'selection4'], factories=[t_facto
qb_2 = qubit_block(p=3, m=5, f=2, t=1, qubit_list=['gancilla_c(0)', 'gancilla_c(1)'], factories=[t_factory_2])
qb_3 = qubit_block(p=4, m=2, f=6, t=1, qubit_list=['gancilla_c(2)', 'gancilla_c(3)'], factories=[t_factory_2])
```

Gate parameters
Qubit allocation
Factory allocation

```
def define_surface_code(p_err=1e-3, pth=0.01, alpha= 0.03, logical_cycle_time=1):
    qecCode = PhysicalQECCode(p_physical = p_err)

    for dist in range(3,51+1,2):
        logical_gate_cycles = {'Idle' : dist, 'T' : dist, 'Clifford' : dist}
        code = {'CodeParams': (dist*dist, 1, dist), 'QubitsPerBlock': 2*dist*dist-1}
        qecCode.addCode(p=p_err, code_parameters=code, logical_cycle_time=logical_cycle_time,
            logical_gate_cycles=logical_gate_cycles, pth=pth, alpha=alpha, code_type='RotatedSurface')
```



# Architecture Models Considered

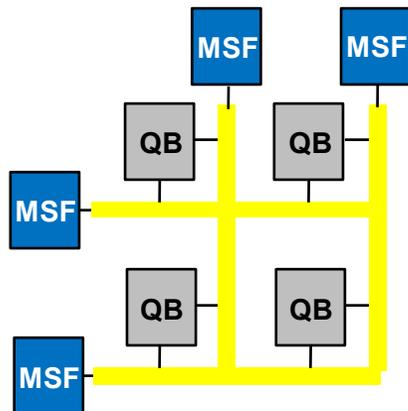
Linear with Private Factories



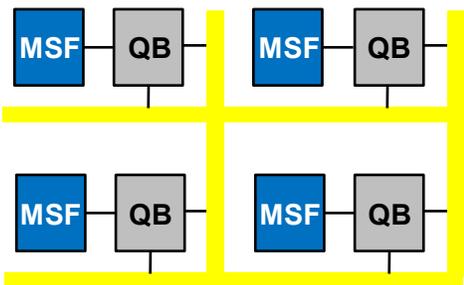
Linear with Shared Factories



Grid with Shared Factories



Grid with Private Factories



Superconducting Qubit

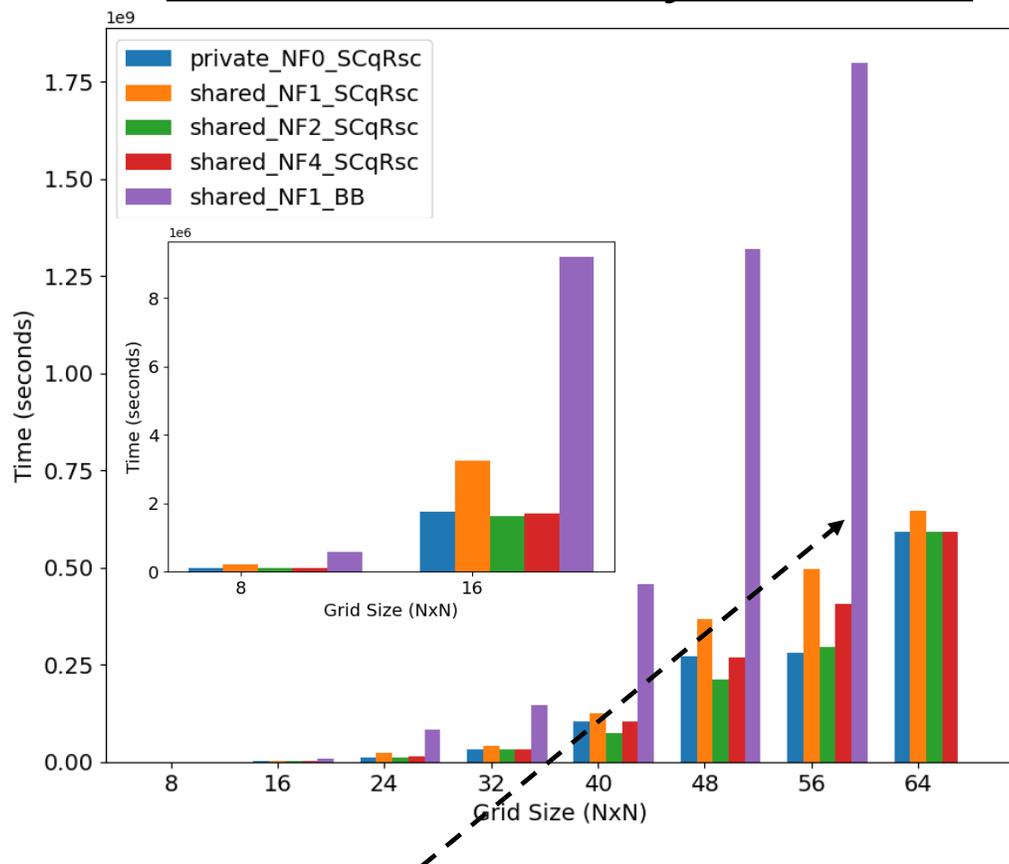
IBM Bicycle

Architecture Parameter	Value	Architecture Parameter	Value
Code type	Rotated surface	Code type	Gross/Two-Gross
Cycle time	1 $\mu$ s	Cycle time	1 $\mu$ s
Physical error	10 <sup>-4</sup>	Physical error	10 <sup>-4</sup>
Architecture type(s)	All	Architecture type(s)	Linear (shared factories)
Qubits per logical block	2*d <sup>2</sup>	Qubits per logical block	400, 768
Intrablock gate limit	unlimited	Intrablock gate limit	1
Inter-block gate limit	1 per free path	Inter-block gate limit	1
Clifford time	d * 1 $\mu$ s	Clifford time	0
T-gate time	d * 1 $\mu$ s	T-gate time	324 $\mu$ s, 617 $\mu$ s
Magic-state factory	Optimized Litinski	Magic-state factory	Optimized Litinski



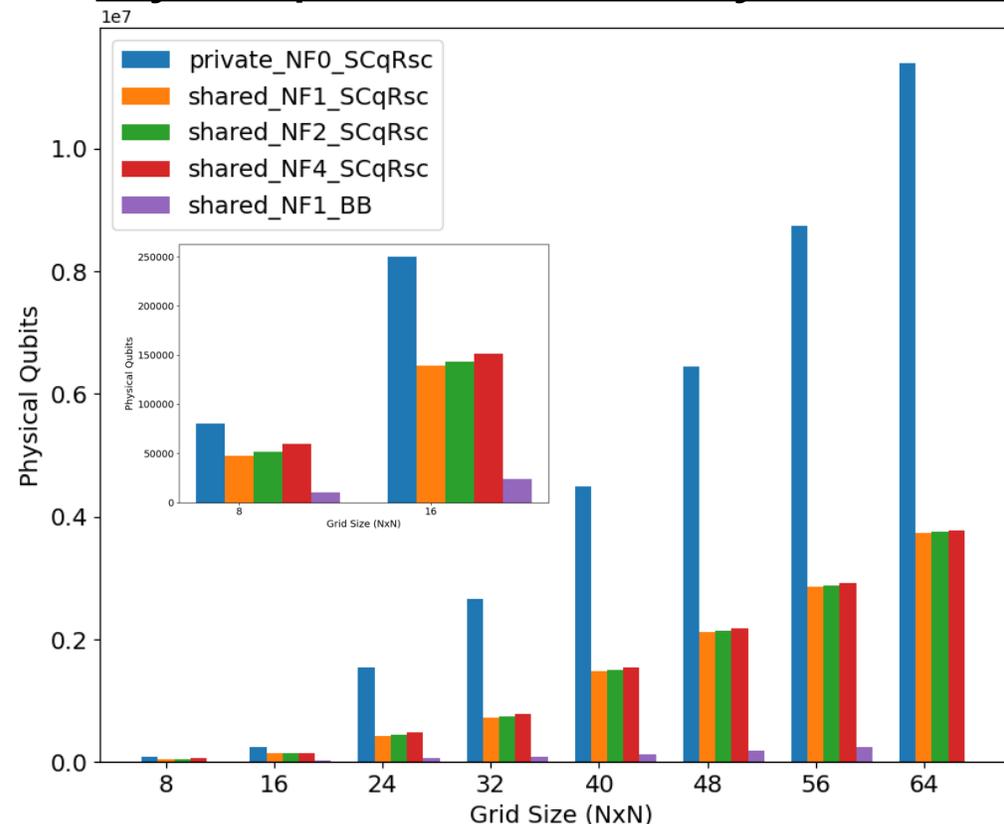
# Runtime and Qubits for Heisenberg Dynamics

## Runtime for one run of dynamics circuit



**Problem size limited  
by achievable error rate**

## Physical qubits for one run of dynamics circuit



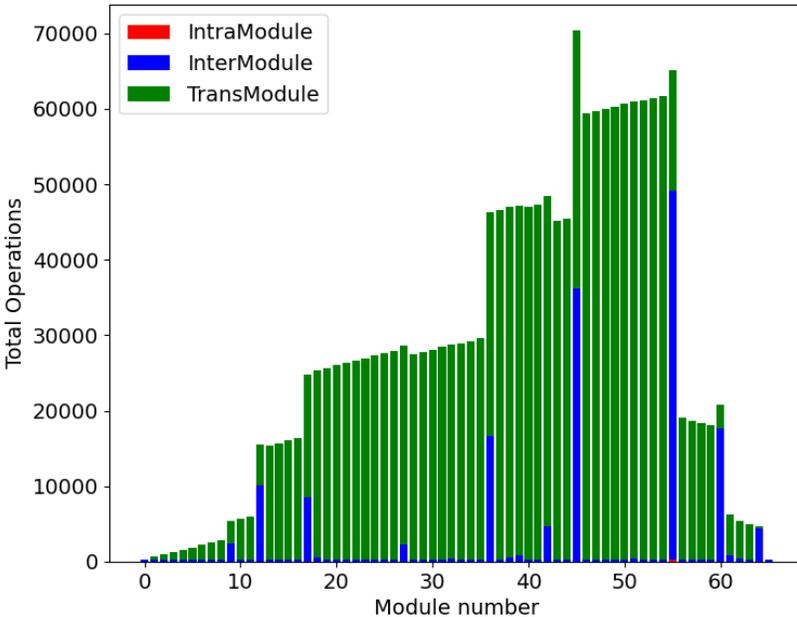
Physical error of  $10^{-4}$

$t_{\max} = 1000$   
 $\epsilon = 10^{-3}$

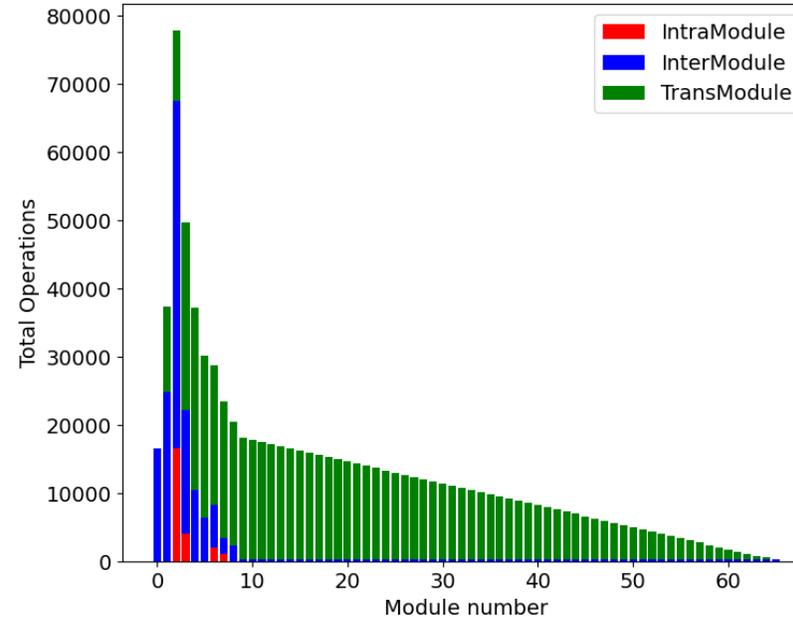


# Impact of Placement on Inter-block Operations

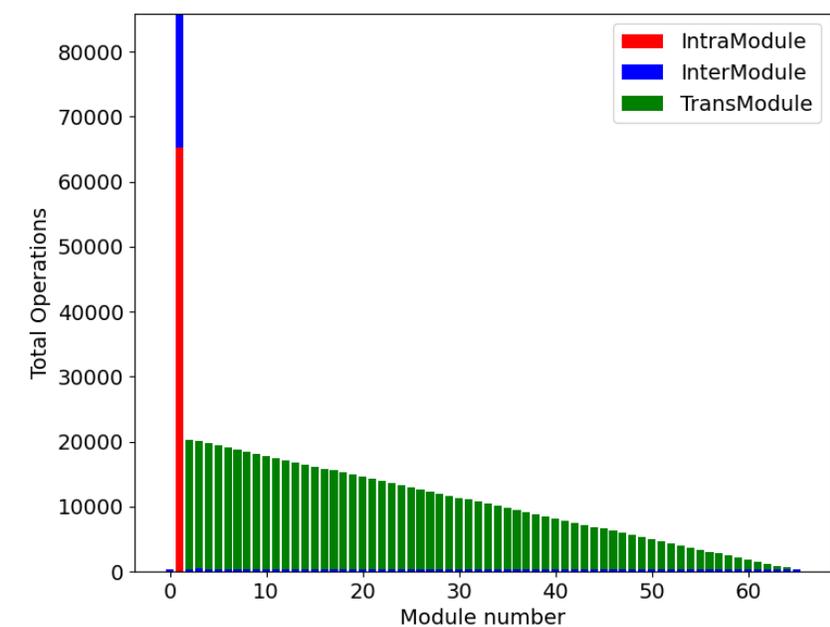
### Random Placement of Qubits to Blocks



### Balanced Placement of Qubits to Blocks



### Localized Placement of Qubits to Blocks



Single block encoding  
of Heisenberg  $N=32$

Module size: 16 qubits  
64 total modules

Types of operations:

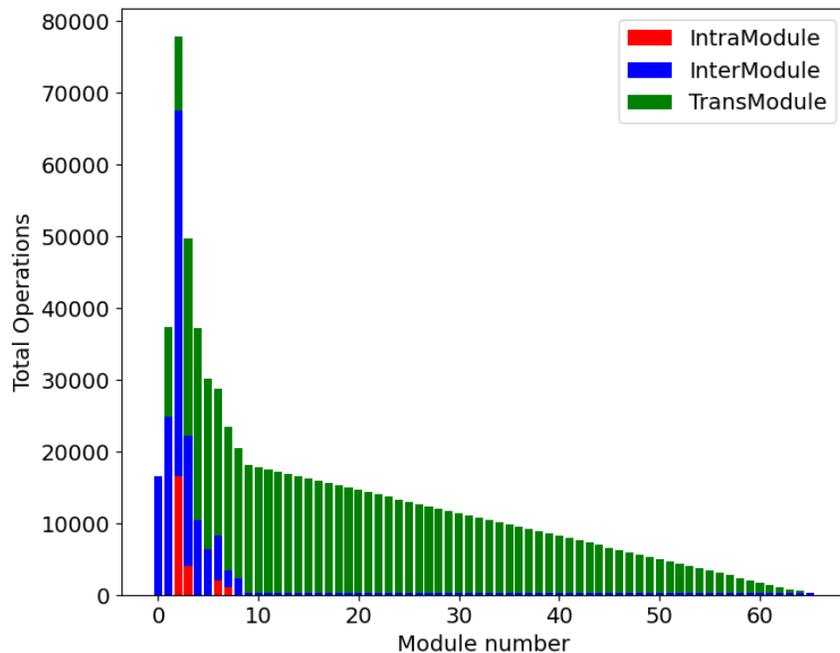
- IntraModule: two-qubit gates within module
- InterModule: two-qubit gates between modules
- TransModule: Operations that transit past modules



# 2D Grid Layout of Modules

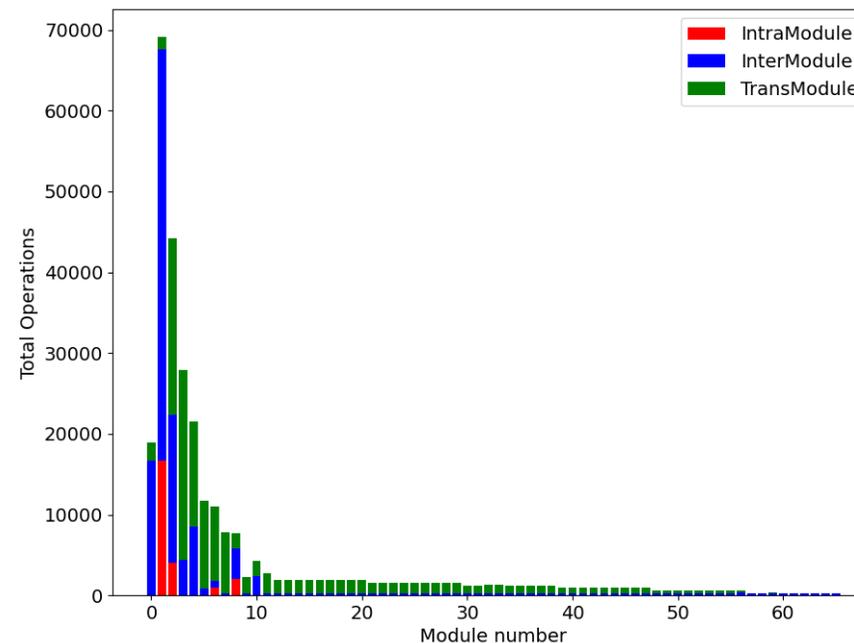
## Linear Layout

**Balanced Placement of Qubits to Blocks**



## 2D Grid Layout

**Balanced Placement of Qubits to Blocks**



Single block encoding  
of Heisenberg  $N=32$

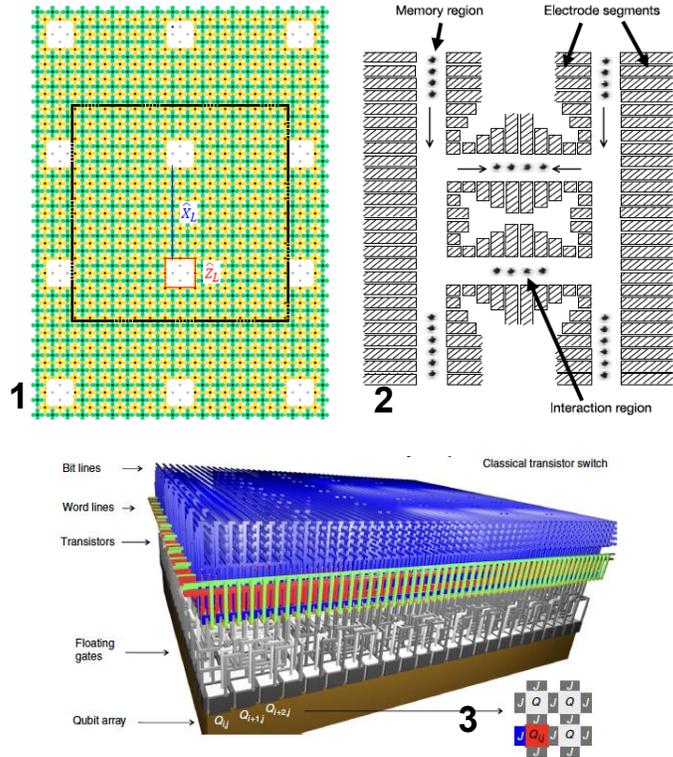
Module size: 16 qubits  
64 total modules

Types of operations:

- IntraModule: two-qubit gates within module
- InterModule: two-qubit gates between modules
- TransModule: Operations that transit past modules

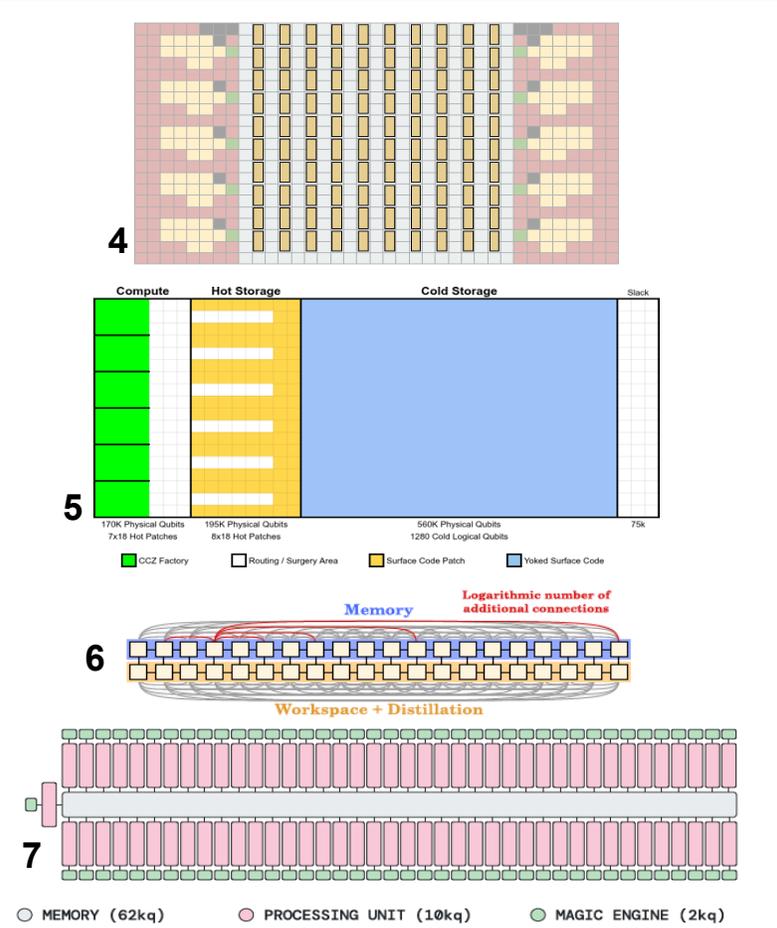


# Challenges and Opportunities Ahead



**Homogeneous technology and uniform design**

- 1) Fowler, et al. "Surface codes: Toward practical large-scale QC." Phys.Rev.A 86, 03324. (2012).
- 2) Kielpinski, et al. "Architecture for a large-scale ion-trap QC." Nature 417 (2002)
- 3) Veldhorst, et al. "Silicon CMOS architecture for a spin-based QC." Nature Comm. 8, #1766 (2017).



**Homogeneous technology and structured design**

- 4) Litinski. "A Game of Surface Codes: Large-Scale QC with Lattice Surgery." Quantum 3, 128 (2019)
- 5) Gidney. "How to factor 2048 RSA integers with less than a million noisy qubits." arXiv: 2505.15917 (2025).
- 6) Caesura, et al. "Faster quantum chemistry simulations on a QC with improved tensor factorization ..." PRX Quantum 6, 030337. (2025)
- 7) Webster, et al. "The Pinnacle Architecture." arXiv:2602.11457



**Heterogeneous technology and structured design**



# Summary/Conclusions

- **Resource estimation requires multiple steps:**
  - Application specification, algorithm selection, circuit generation, resource counting
- **Circuit-level analysis of architectures will have an increasing impact on fault-tolerant architecture develop in the future**

**pyLIQTR:**  
pip install pyliqtr or  
<https://github.com/isi-usc-edu/pyLIQTR>

**Lincoln laboratory is hiring:**

- QEC experts
- Algorithm design, optimization and analysis

Thanks to the Lincoln Laboratory quantum algorithms team!

**Justin  
Elenewski**



**Kaitlyn  
Morrell**



**Parker  
Kuklinski**



**Ben  
Rempfer**



**Arthur  
Kurlej**



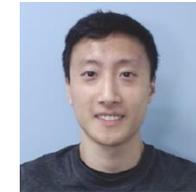
**Rylee  
Neumann**



**Rob  
Rood**



**Daniel  
Liang**



**Maria  
Prado Rodriguez**

